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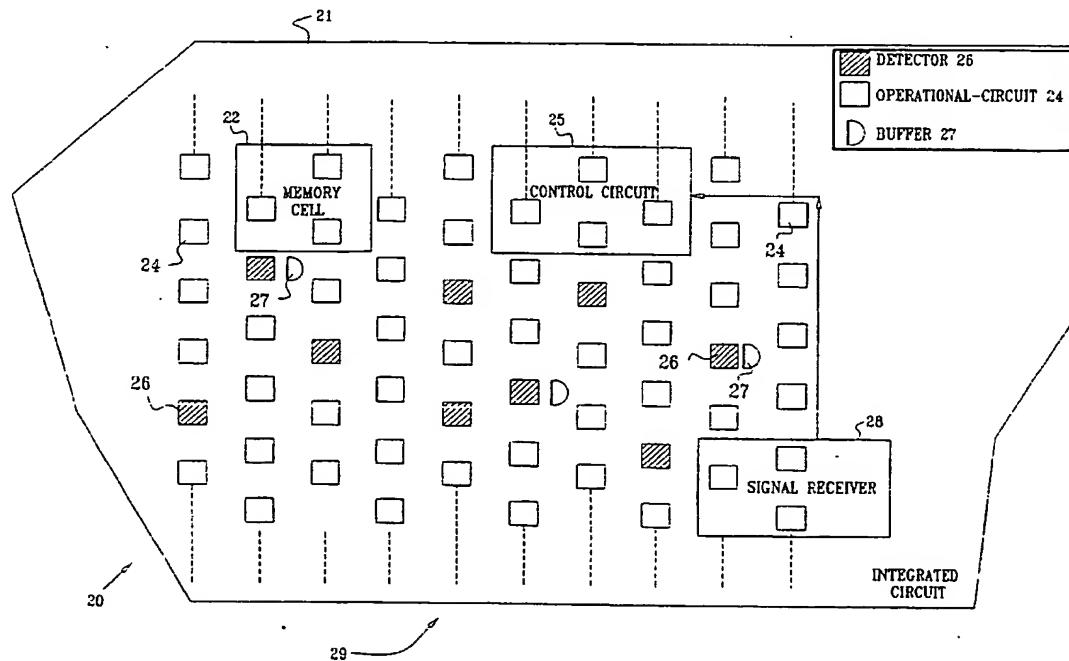
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(54) Title: PREVENTION OF TAMPERING IN ELECTRONIC DEVICES

**WO 03/102510 A2**

(57) Abstract: Circuitry for protection of an integrated circuit (20) which includes operational-circuits (24) formed on a chip (21). The circuitry includes a plurality of detectors (26), integrally formed on the chip as part of the integrated circuit and dispersed among the operational-circuits on the chip. The detectors are adapted, in response to radiation incident on the chip, to trigger a security measure so as to prevent tampering with the integrated circuit.